



المعهد العالى للحاسبات وتكنولوجيا المعلومات مدينة الشروق - القاهرة شعبة علوم الحاسب

Course specification

Course Code: CS 220

Course Title: Computer Organization & Assembly

Programming

Academic Year: 2022/2023

Course specification

(CS 220 – Computer Organization & Assembly Programing)

	Course Outline											
Faculty:	HICIT- (Higher Institute for Computers & Information Technology-El Shorouk Academy)											
Programm	e(s) on which the course is given:	Undergraduate program in Computer Science										
Major or m	ninor element of programme:	Compulsory										
Departmen	t offering the program	Department of Computer Science										
Departmen	t offering the course:	Department of Computer Science										
Level		Second Level										
Date of spe	cification approval	DD/MM/2023										

			Basic Infor	rmation								
Code: CS 220 Title: Computer Organization & Assembly Programming												
Prerequisites: CS 121 Logic Design, Electroncis												
Weekly H	ours:											
Lecture: 2	2	Exercise	: -	Practical: 2 Total: 3 credit hours								

Professional Information

Course Aims:

This course covers basic topics about computer architecture and organization. The course provides the study of the structure, characteristics and operation of modern-day computer systems including a basic background on the computers evolution, its design process and its internal characteristics which includes processor components, control unit architecture, memory organization and system organization. All internal components of a computer including processors, cache memories, random access memories, magnetic disks, optical memories and input/output connections are considered from an architectural perspective. Integer and floating-point representation in arithmetic logic unit (ALU) with arithmetical operations are explained. Operating system principles and are also described After completing this course students must be able to:

- ter completing this course students must be able to.
- Describe the basic organization of microprocessor-based systems.
- Develop correct, efficient and easily maintainable programs written in a low-level language.
- Discuss the interface between the software and the hardware, and Illustrate how does software instruct the hardware to perform needed functions.
- Determine the performance of a program, and illustrate how to improve the performance.
- Apply the techniques that can be used by hardware designers to improve performance.
- Determine the techniques that can be used by hardware designers to improve energy efficiency.
- Discuss the reasons for and the consequences of the recent switch from sequential processing to parallel processing.

Knowledge and understanding	Intellectual Skills	Professional and practical skills	General and Transferable skills
A8, A10, A15, A18	B7, B8, B12, B16	C1, C6, C9, C14	D1, D3, D4, D10

Intended learning outcomes of course (ILOs)

A. Knowledge and Under-Standing:

- a1. Understand the basic organization of microprocessor-based systems, the interface between the software and the hardware, and illustrate how does software instruct the hardware to perform needed functions. [A8, A10]
- a2. To understand the blueprint of the architecture of a computer. [A8]
- a3. To explain hardware concepts of small and large computers. [A8]
- a4. Understand the merits and pitfalls in computer performance measurements. [A18]
- a5. Understand the impact of instruction set architecture on cost-performance of computer design. [A8, A10]
- a6. Understand ways to take advantage of instruction level parallelism for high performance processor design. [A8, A15]
- a7. Understand dynamic scheduling methods and their adaptation to contemporary microprocessor design. [A10, A15]
- a8. Understand the impact of branch scheduling techniques and their impact on processor performance. And memory hierarchy and its impact on computer cost/performance. [A8, A10, A15]

B. Intellectual Skills:

- b1. How to think about a micro-program. [B7, B8, B12]
- b2. Determine the techniques that can be used by hardware designers to improve energy efficiency. [B7, B12]
- b3. Discuss the reasons for and the consequences of the recent switch from sequential processing to parallel processing. [B8, B12, B16]

C. Professional and practical skills

- c1. Develop correct, efficient and easily maintainable programs written in a low-level language. [c1]
- c2. Determine the performance of a program and illustrate how to improve the performance. [C1, C6]
- c3. Apply the techniques that can be used by hardware designers to improve performance and discuss the reasons for and the consequences of the recent switch from sequential processing to parallel processing. [C1, C6, C9,C14]

D. General and transferable skills

- d1. Verbally demonstrate and communicate findings on processor simulation. [D1, D3]
- d2. Work as a team on a processor design and simulation project. [D1, D3, D4, D10]
- d3. Apply specific task in certain period. [D3, D4]

Contents												
TDi.	Contact I	Hours										
Торіс	lecture	Lab										
Introduction to C.O.A and Representation of Basic Information	6	3										
Computer Evolution and Performance.	6	3										
A Top-Level View of Computer	6	4										
Cache Memory	6	3										
Internal Memory	6	6										
External Memory	6	4										
Operating System Support	6	6										
Course project	3	3										

Teaching and learning methods	
Teaching and learning methods	Used
Lectures	√
Tutorial Exercises	V
Practical Lab	
Discussions.	$\sqrt{}$
Self – Learning (Reading material, Websites search,)	-
Self-studies	-
Group work	-
Presentation	-
Problem solving/problem solving learning based	-
Case study	-
Synchronous E-Learning	-
Video lectures	-
Asynchronous E-Learning	-

Student assessment methods & Schedule										
Methods	Used	Week#								
Midterm Exam		8								
Final Exam		16								
Course Work & Quizzes		2-14								
Practical Exam	V	15								

Assessment Weight	
Assessment	Weight %
Mid Term Exam	10%
Practical Exam and Project	10%
Final Exam	70%
Course Work & Quizzes	10%
Total	100
Course Work &Quizzes	
Short Exams, Assignments, Researches, Reports, Presentations	
Class/Project discussion	

	List of references
Essential books (textbooks)	 Principe, Euliano, and Lefebvre, "Neural and Adaptive Systems: Fundamentals through Simulations", John Wiley and Sons, ISBN: 0471351679. Laurene Fausett, "Fundamentals of Neural Networks Haykin, Neural Networks: A Comperhensive Foundation
Course notes	E-Learning Portal
Recommended books	 Title: Computer Organization and Architecture: Designing for Performance Authors: William Stallings, Publisher: Prentice-Hall India Title: Computer Organization Authors: Carl Hamacher, Zvonko Vranesic and Safwat Zaky Publisher: McGraw Hill
Periodicals, website	Internet Locationhttp://en.wikipedia.org/wiki/Operations_research
Videos link	

Required Facilities												
Tools & SW (Technology	- Python, TensorFlow											
facilities):	-											
	Whiteboard	$\sqrt{}$										
	Computer Lab	$\sqrt{}$										
Teaching facilities:	Data show	$\sqrt{}$										
Teaching facilities.	E-Learning	$\sqrt{}$										
	Videos	$\sqrt{}$										
	Website	$\sqrt{}$										

	Course Content/ILO Matrix																
Course Contents	K	nowl	edge	& uı	nder	stai	ndin	ıg		ellec skill:	tual s	Professional and practical skills			General		
	a1	a2	a3	a4	a5	a6	a7	a8	b1	b2	b3	c1	c2	c3	d1	d2	d3
Introduction to C.O.A and Representation of Basic Information	X	Х	Х	Х	Х				Х	Х	X						
Computer Evolution and Performance.				Х	X	X			Х	X	X			X		Х	
A Top-Level View of Computer	X	X	X	X	X							X					
Cache Memory				Х	X	X	X	X		X		X	X				X
Internal Memory				X	X	X	X	X		X			X		X		
External Memory		X			X	X	X			X					X	X	
Operating System Support			X		X	X	X	X	X	X							
Course project	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X

	Learning Method /ILOs Matrix																
Learning Methods		now	ledge	e and	l und	lerst	andi	ing		ellect skills		_	essiona ctical s		General		
8	a1	a2	a3	a4	a5	a6	a 7	a8	b1	b2	b3	c1	c2	c3	d1	d2	d3
Lectures	X	X	X	X	X	X	X	X	X	X	X	X	X		X	X	X
Tutorial Exercises	X	X	X	X	X	X	X	X	X	X	X	X	X				
Practical Lab									X	X	X	X	X	X			X
Discussions									X	X		X	X		X	X	X

	Assessment Methods /ILOs Matrix																	
Assessment	Kn	owle	edge	and	l un	derst	and	ing	Intel	lectua	l skills		ssiona tical s			General		
Methods	a1	a2	a3	a4	a5	a6	a7	a8	b1	b2	b3	c1	c2	c3	d1	d2	d3	
Mid Term Exam	X	X	X	X					X	X		X	X					
Final Exam	X	X	X	X	X	X			X	X								
Course Work &Quizzes	X	X	X	X	X	X			X	X		X	X		X	X	X	
Practical Exam	X	X	X	X	X	X			X	X		X	X					

Prog ILOs Course ILOs		Knowledge and understanding				Intellectual skills				Professional and practical skills				General			
					-8												
		A8	A10	A15	A18	В7	В8	B12	B16	C1	С6	С9	C14	D1	D3	D4	D10
Knowledge and	a1	X	X														
Understanding	a2	X															
	a3	X			X												
	a4	X															
	a5	X	X	X	X												
	a6	X		X													
	a7		X	X													
	a8	X	X	X													
Intellectual skills	b1					X	X	X									
	b2					X		X									
	b3						X	X	X								
Professional and	c1									X							
practical skills	c2 c3									X X	X X	X	X				
General skills	d1													X			
	d2													X	X	X	X
	d3													X	X		

Course Coordinator : Dr. Tarek S.AbdElazim ()
Head of Department : Assoc. Prof. Ahmed El-Abbassy	()
Date://2023		